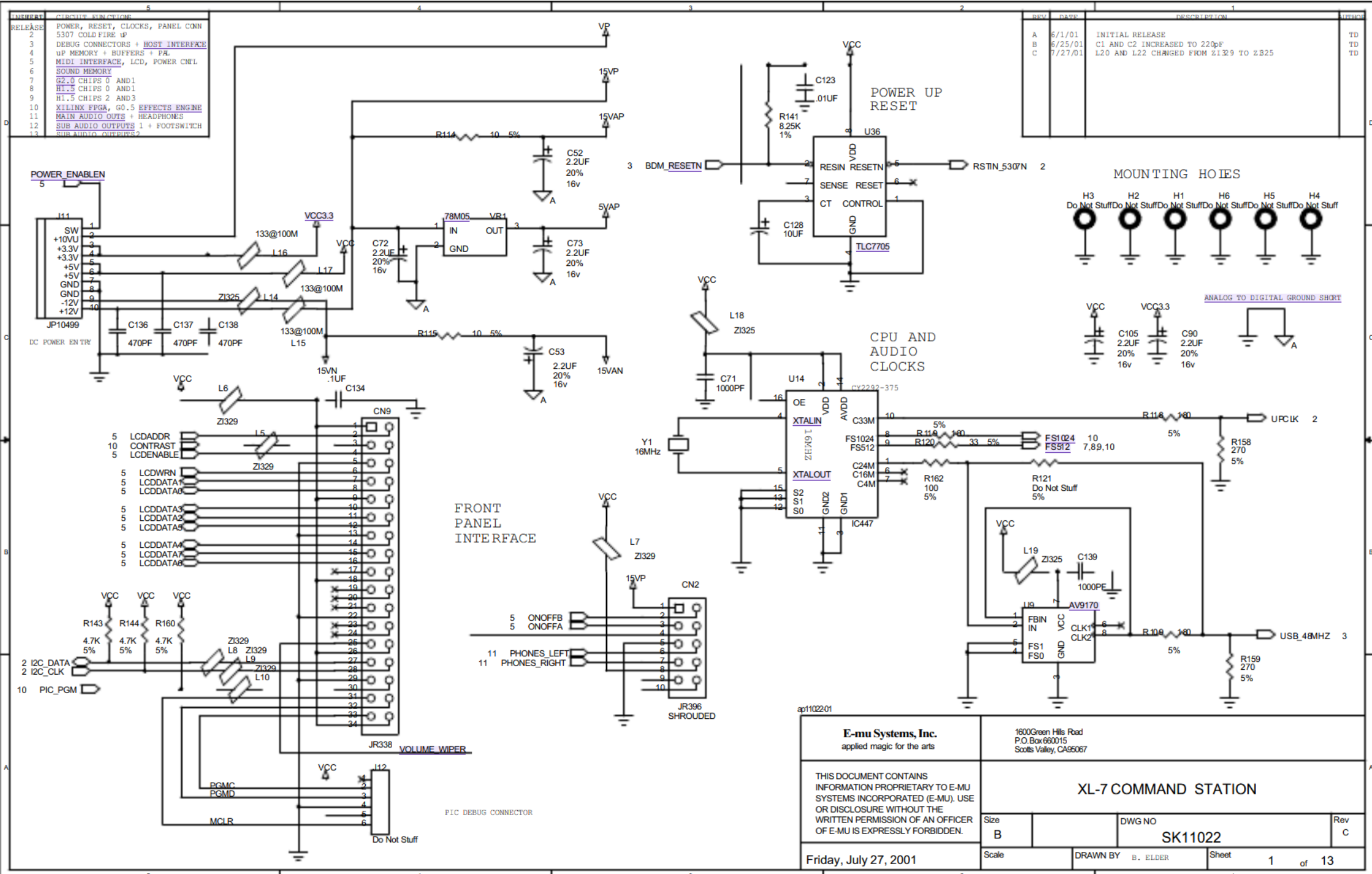
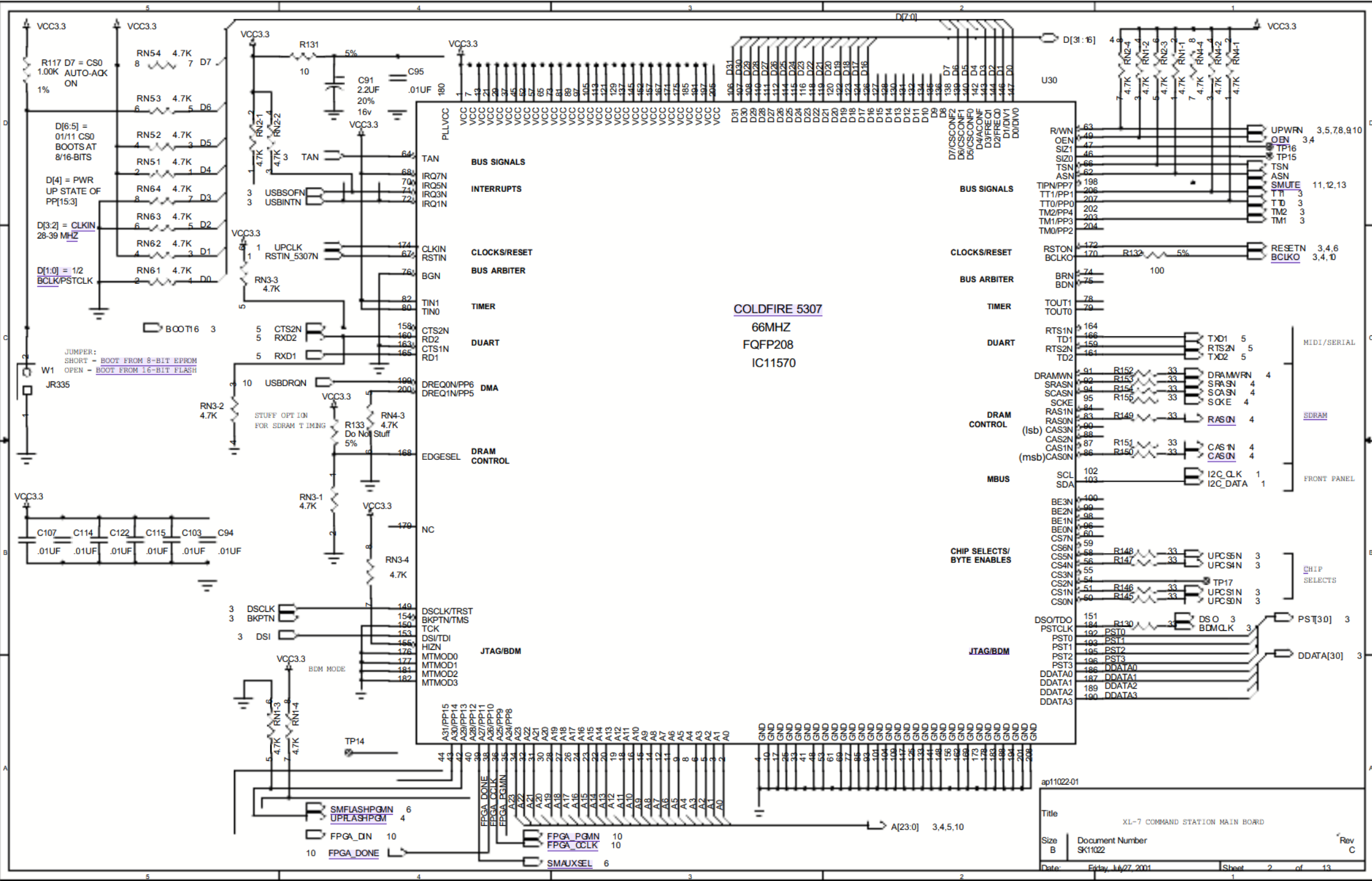
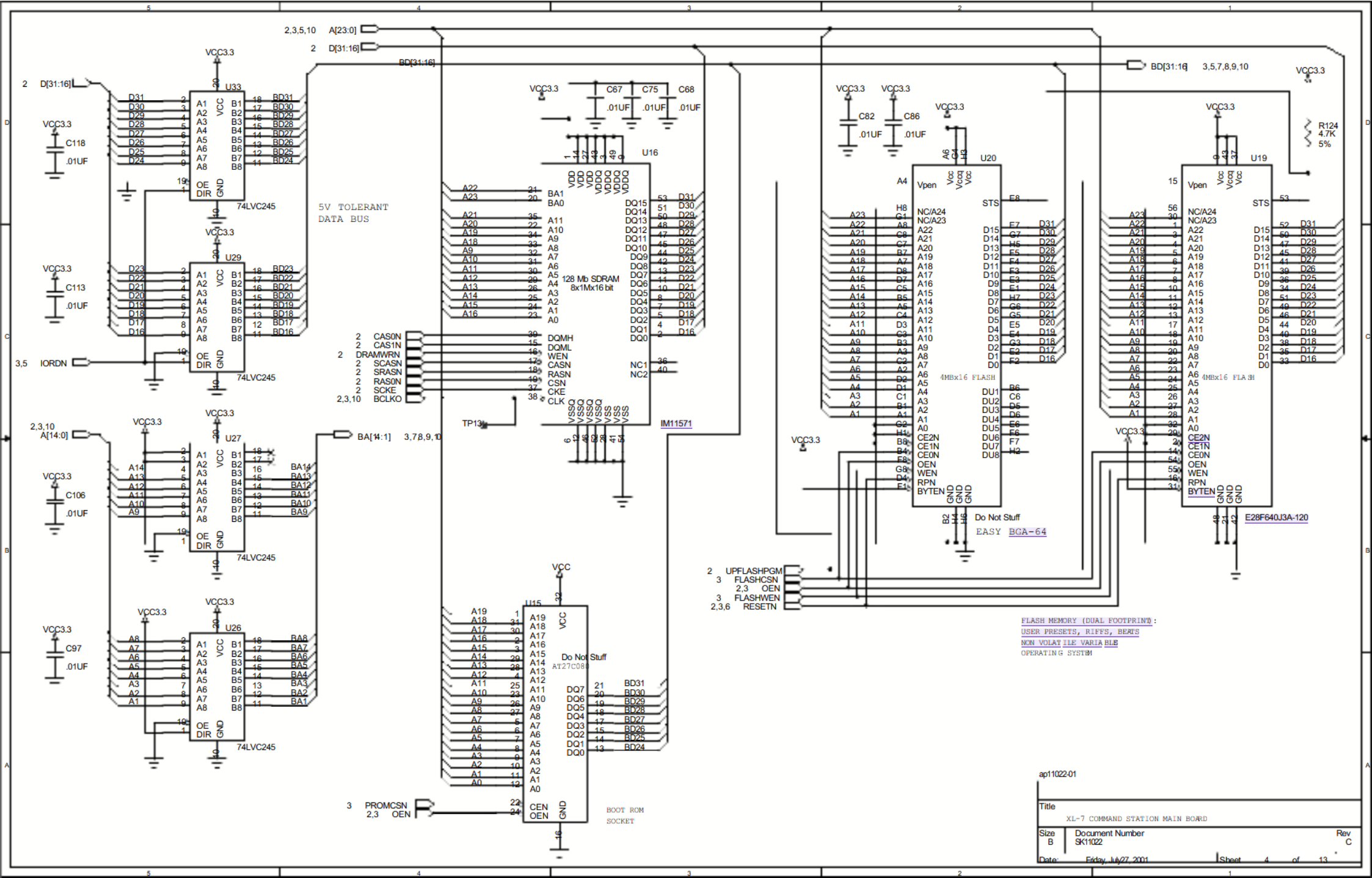






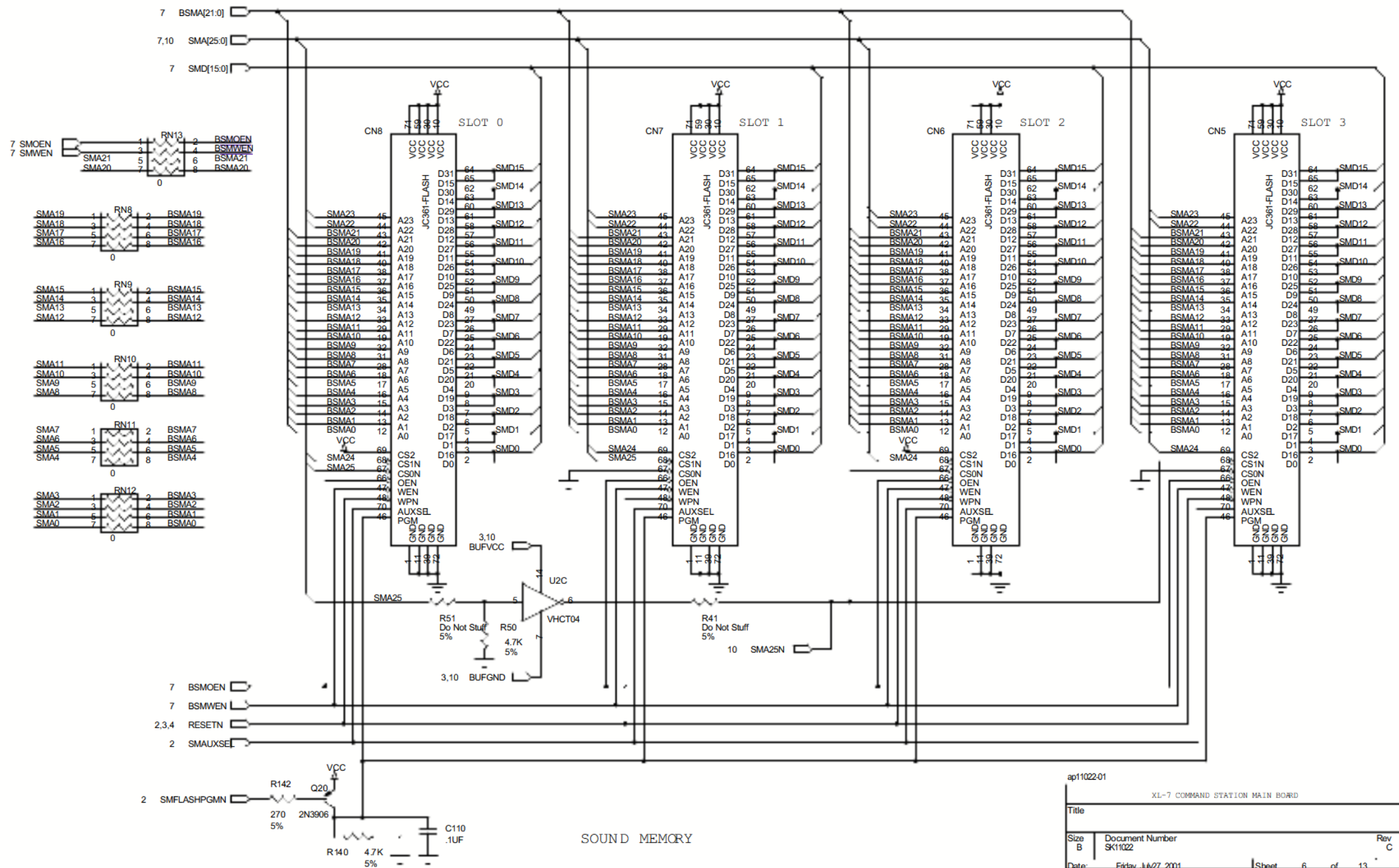
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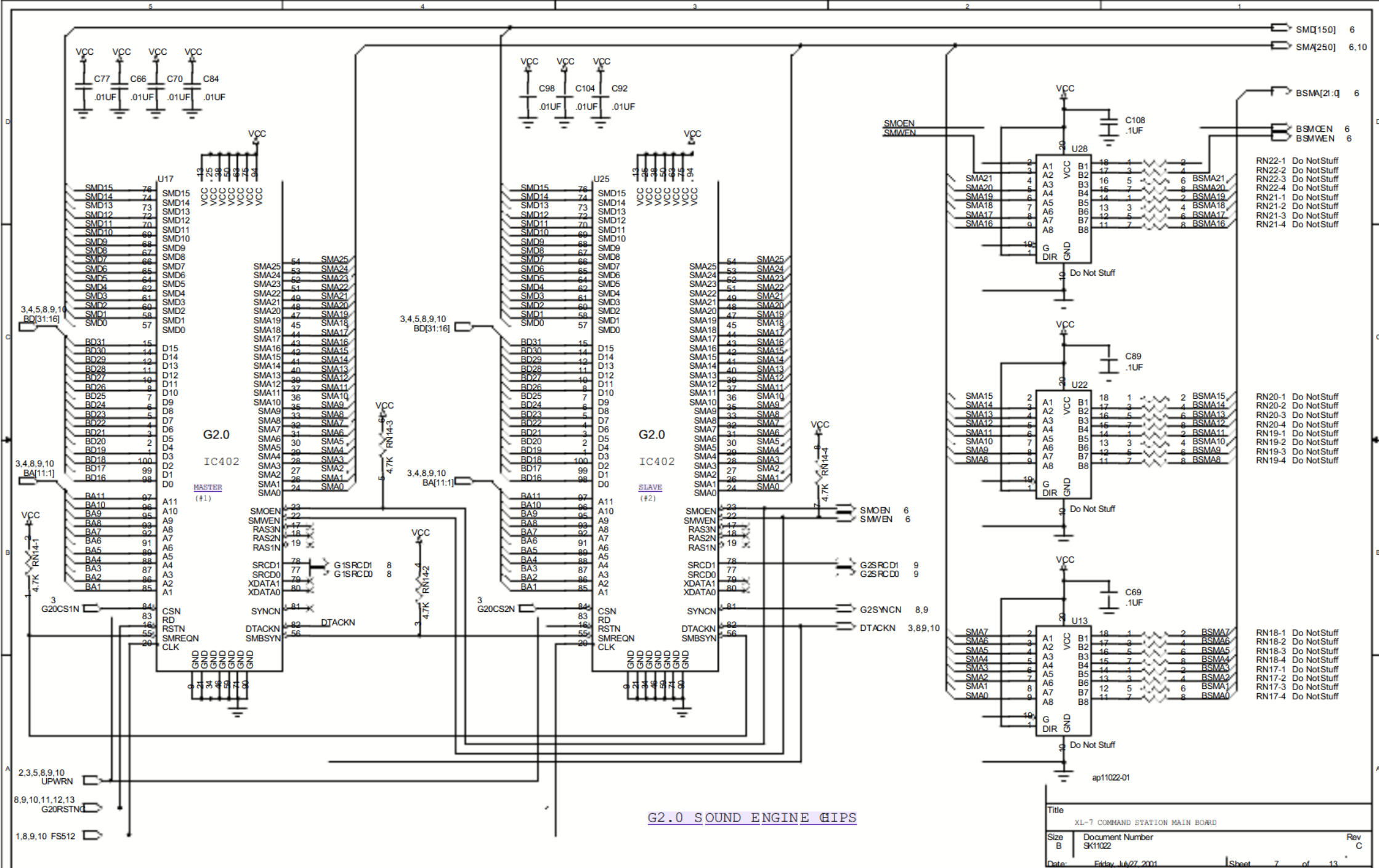


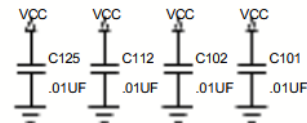
FLASH MEMORY (DUAL FOOTPRINT):
USER PRESETS, RIFFS, BERTS
NON VOLATILE VARIABLE
OPERATING SYSTEM

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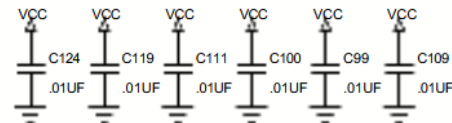
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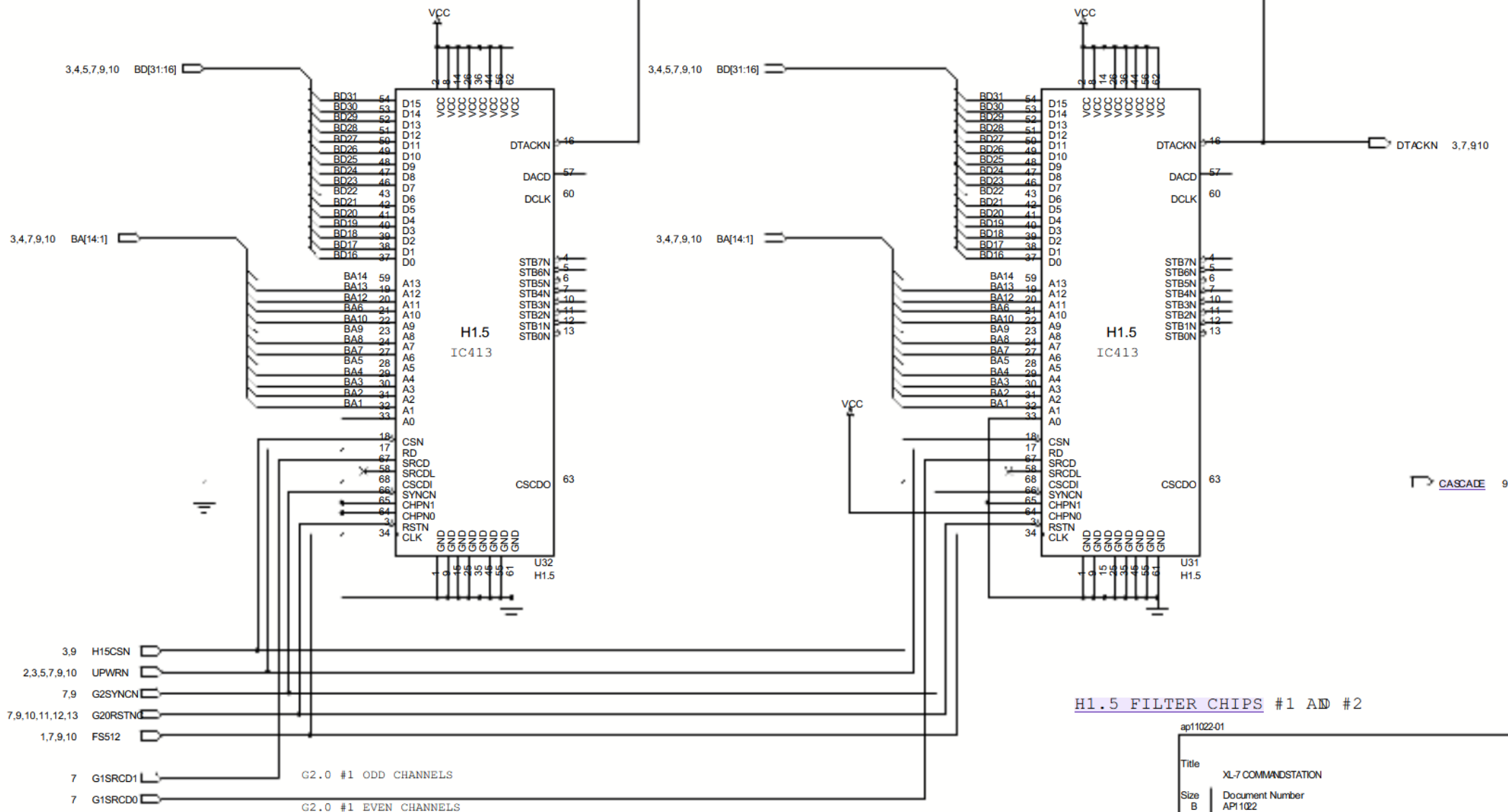




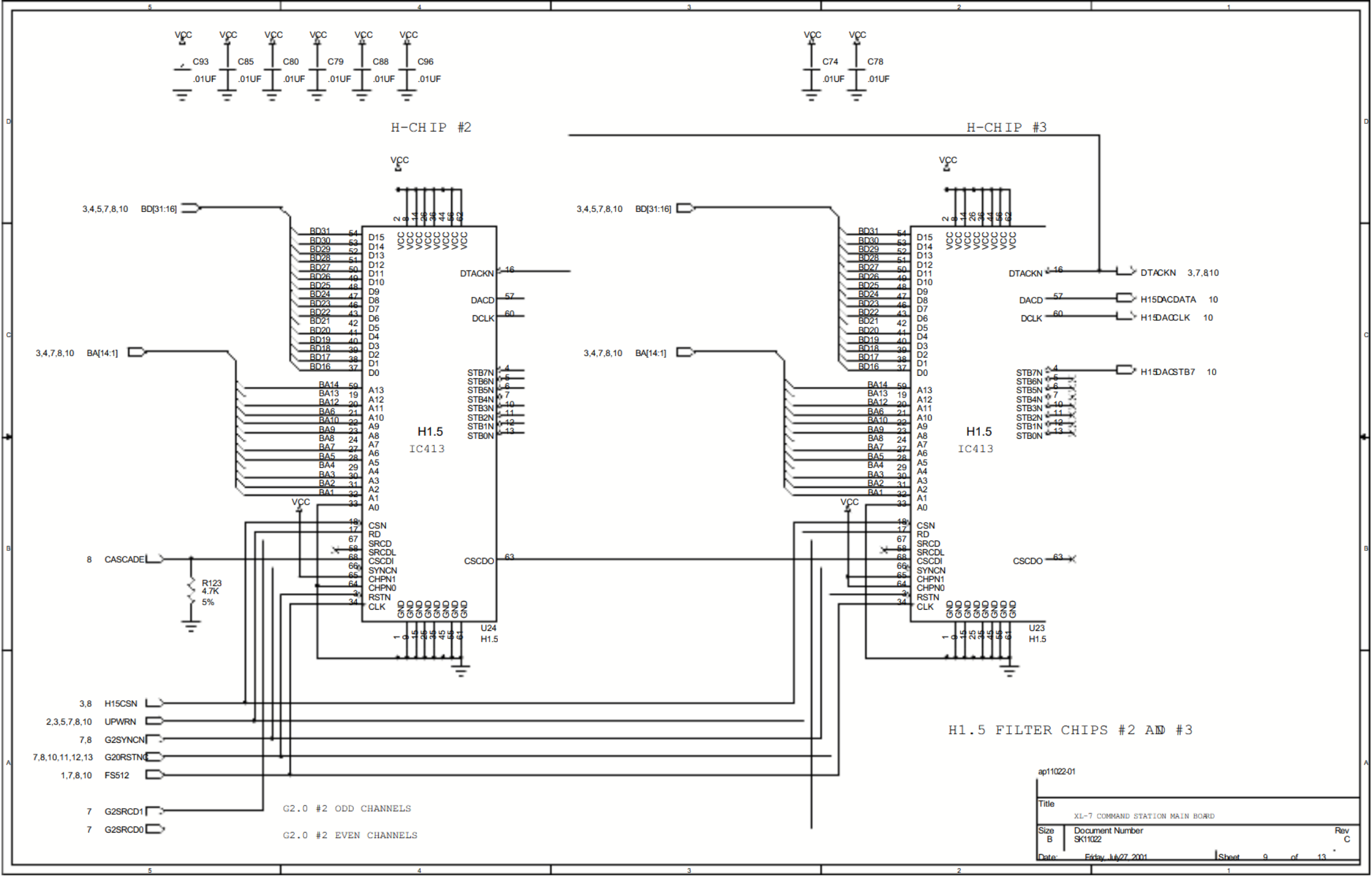
H-CHIP #0



H-CHIP #1



H1.5 FILTER CHIPS #1 AND #2

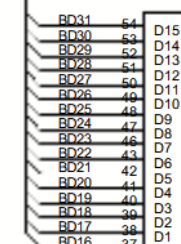
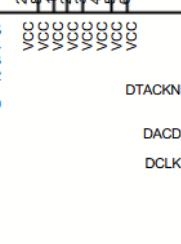
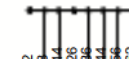
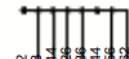


H-CHIP #2

H-CHIP #3

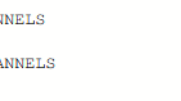
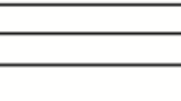
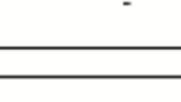
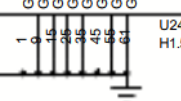
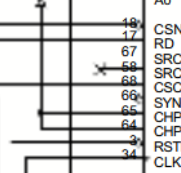
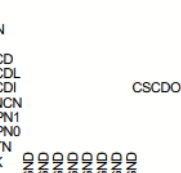
VCC

VCC



H1.5
IC413

H1.5
IC413



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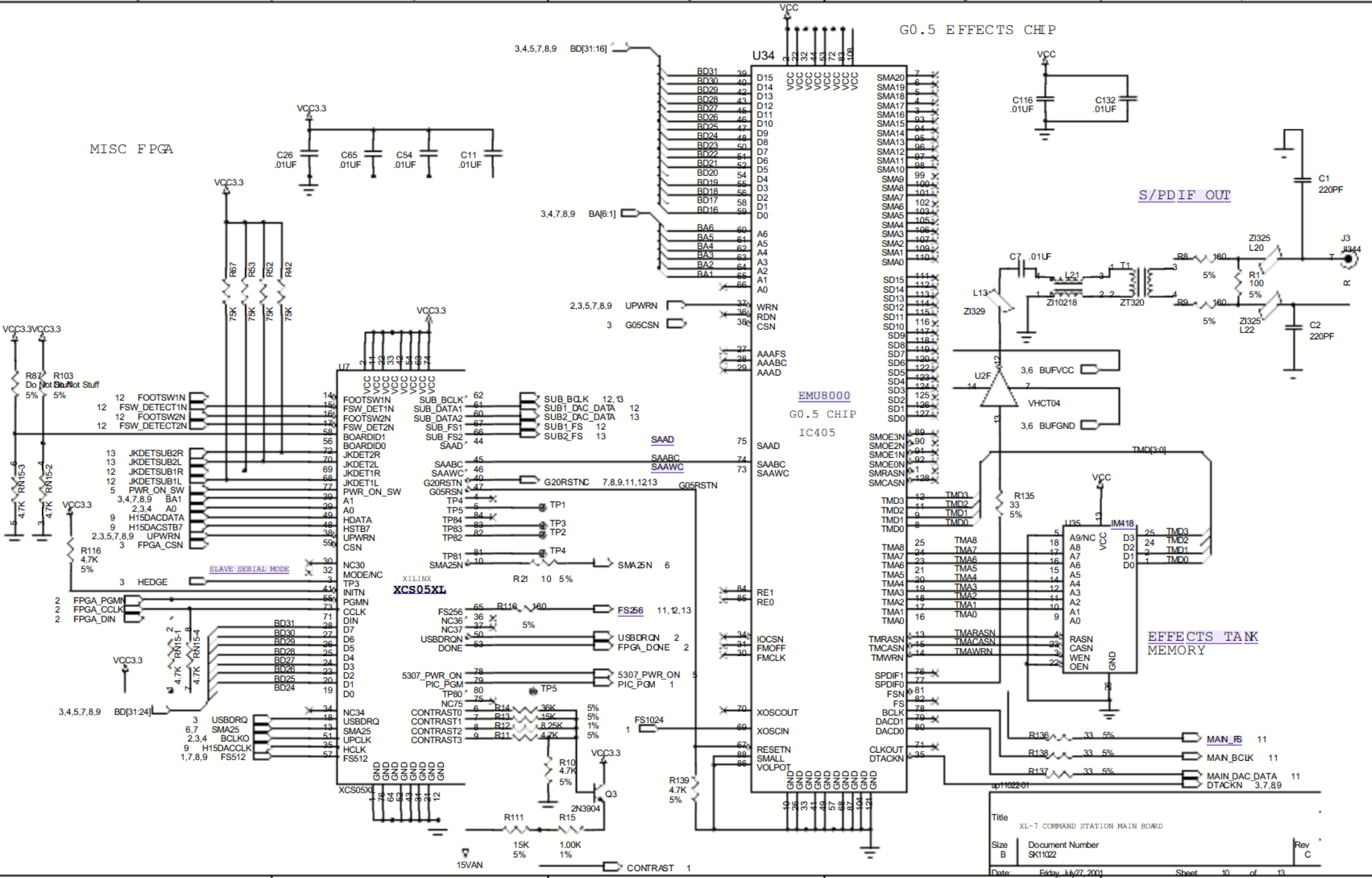
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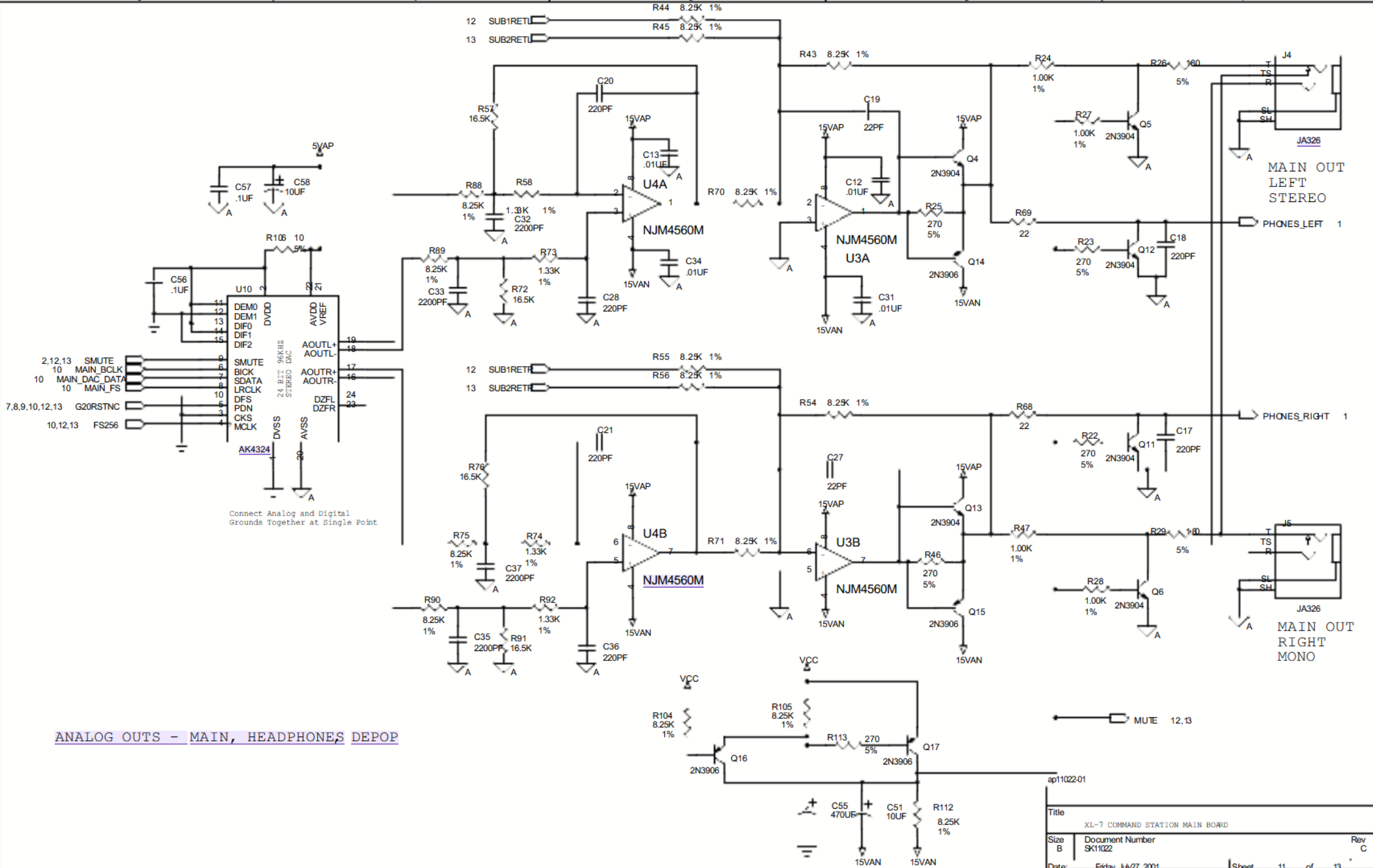
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G0.5 EFFECTS CHIP

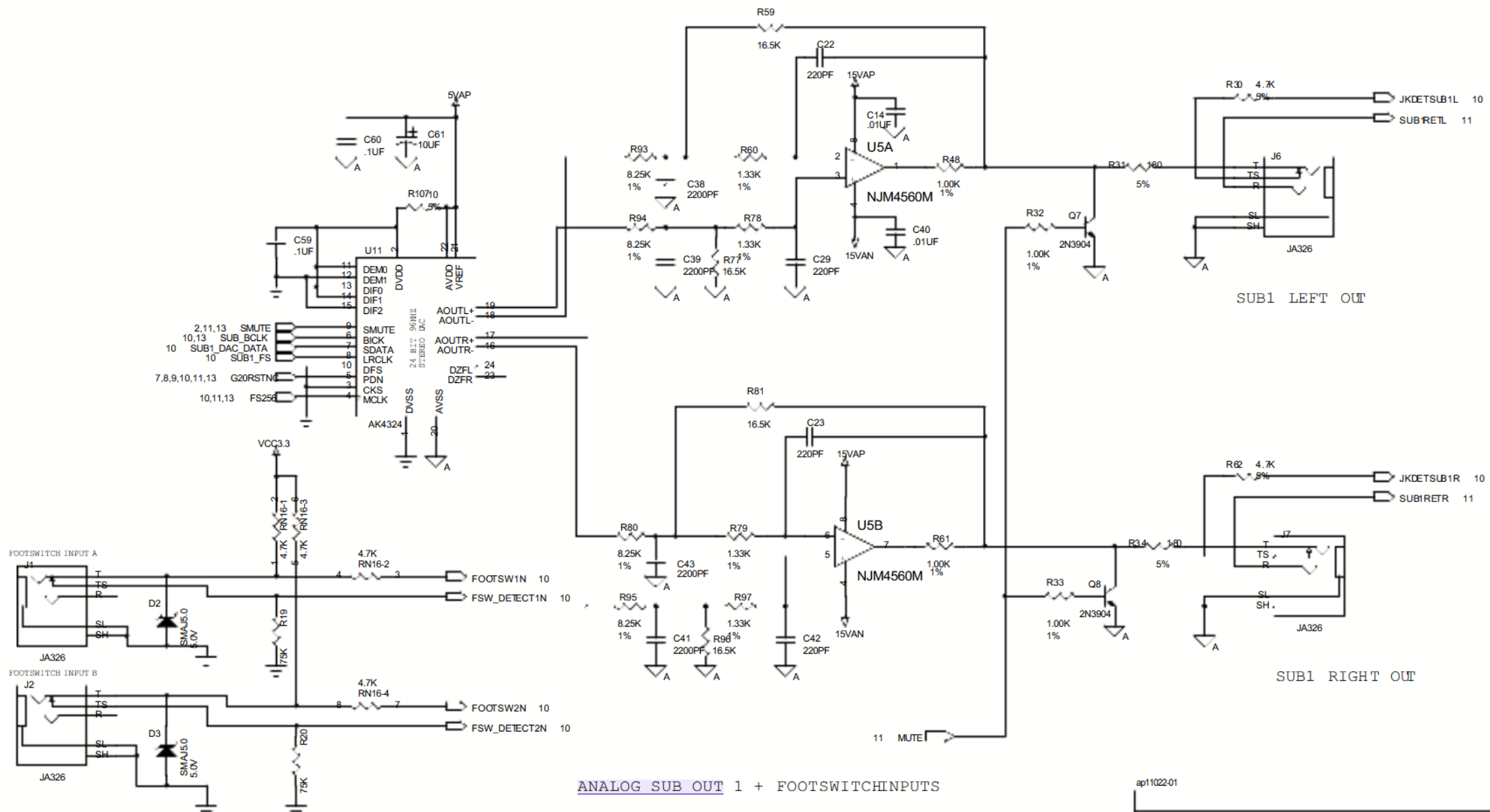
MISC FPGA





ANALOG OUTS - MAIN, HEADPHONES DEPOP

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ANALOG SUB OUT 1 + FOOTSWITCHINPUTS

