



深圳市晶彩智能有限公司  
Shenzhen Jingcai Intelligent Co., Ltd

APPROVAL SHEET  
承认书

Customer 客户名称	
Part NO. 产品型号	JC8048B070N
Product type 产品内容	Mode: Transmissive type .Normally white. TFT LCD Module LCD Module: Graphic 800RGB*480Dot-matrix
Remarks 备注栏	<input type="checkbox"/> Approval for Specification Only <input checked="" type="checkbox"/> Approval for Specification and Sample
Signature by Customer: 客户确认签章	

Issued by	Checked by	Approved by

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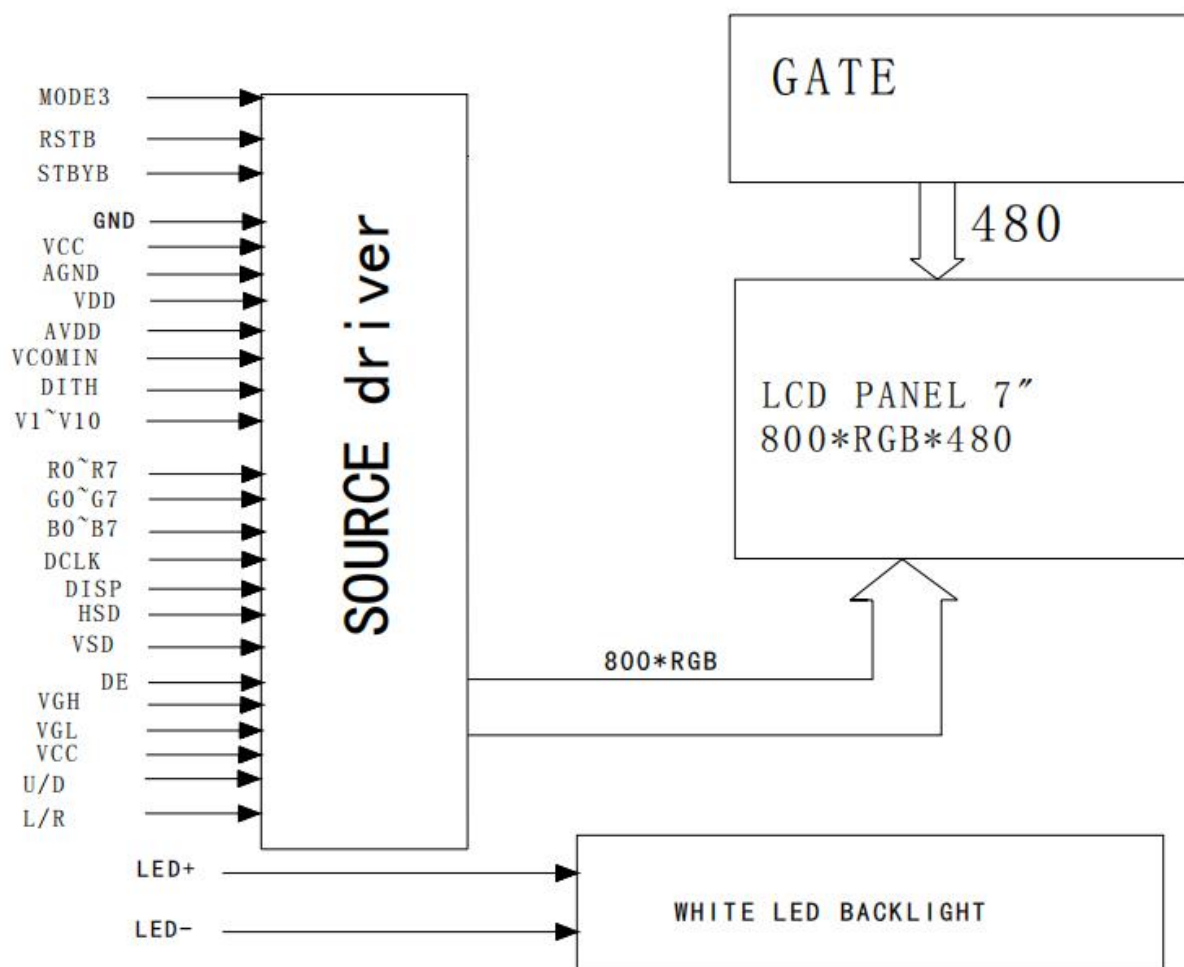
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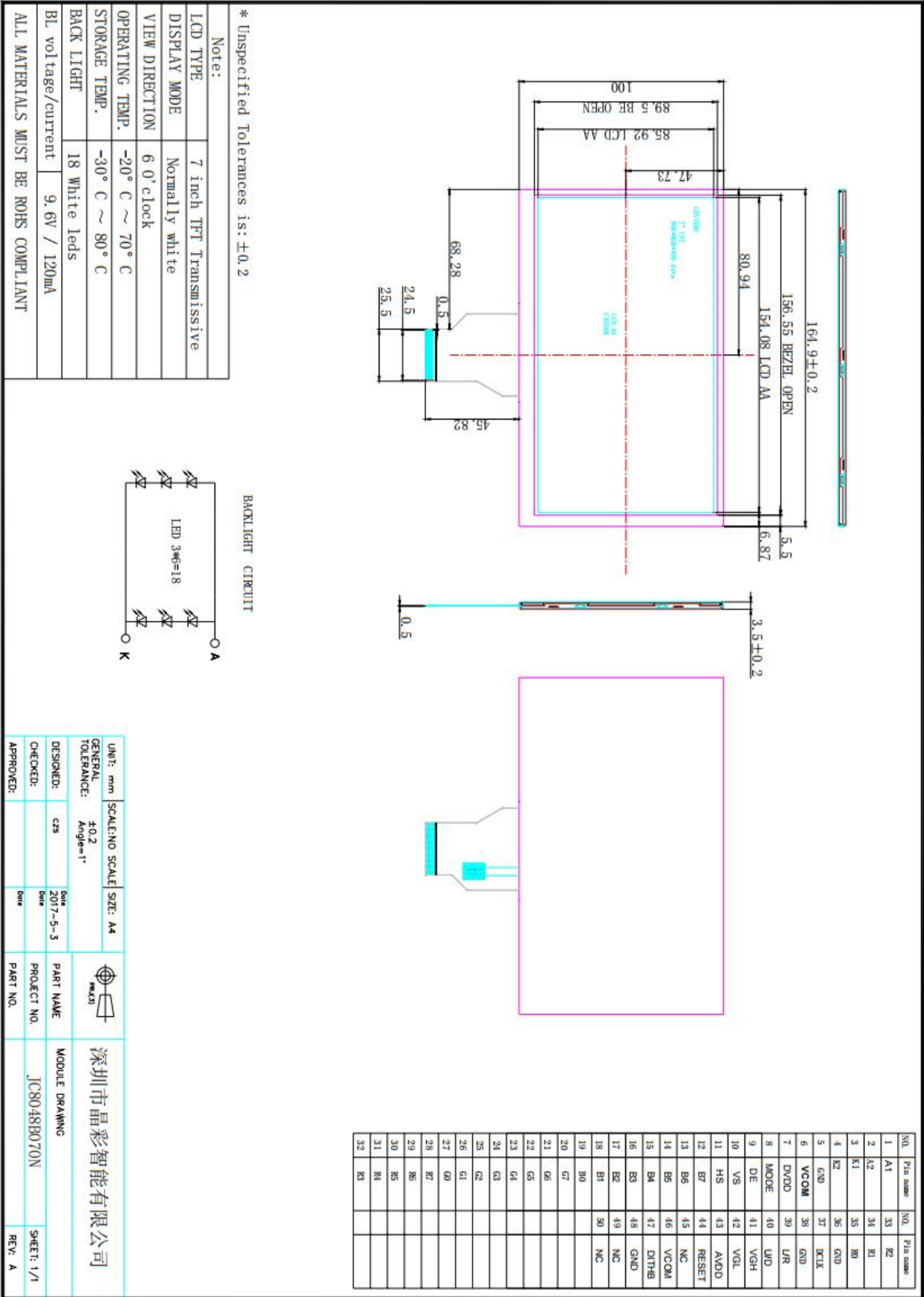
## 1. PHYSICAL DATA

Item	Contents	Unit
LCD type	TFT TRANSMISSIVE	---
Viewing direction	6	o'clock
Module size (W·H·T)	164.9 × 100 × 3.5	mm <sup>3</sup>
Active area(W×H)	154.08×85.92	mm <sup>2</sup>
Number of dots(W×H)	800(RGB) × 480	dots
Pixel Pitch(W×H))	0.1926(H)×0.179(V)	mm
Colors	16M	---
Backlight Type	18 white leds 9.6V/120mA	---
Interface Type	RGB	---

## 2. BLOCK DIAGRAM



### 3. Mechanical Dimension



## 4. Pin Descriptions

Pin No.	Symbol	Functional
1	LED A	LED Anode
2	LED A	LED Anode
3	LED K	LED Cathode
4	LED K	LED Cathode
5	GND	Digital Ground
6	VCOM	For external VCOM DC input
7	DVDD	Digital Power
8	MODE	DE/SYNC mode select MODE=H: DE mode( normally pull high) MODE=L: HSD/VSD mode
9	DE	Data enable signal
10	VSYNC	Vertical sync input.
11	HSYNC	Horizontal sync input
12~19	B7~B0	Blue data Input
20~27	G7~G0	Green data Input
28~35	R7~R0	Red data Input
36	GND	Digital Ground
37	DCLK	Clock input
38	GND	Digital Ground
39	L/R	Source right or left sequence control SHLR=H: right shift, Left t Right SHLR=L: left right, Right t Left
40	U/D	Gate up or down scan control UPDN=H: up shift, Down t Up UPDN=L: down shift, Up t Down
41	VGH	Positive Power for TFT
42	VGL	Negative Power for TFT
43	AVDD	Analog Power
44	RSTB	Global reset pin.Active low to enter reset state Suggest to connecting with an RC reset circuit for stability. Normally pull high. (RC circuit :R=10K $\Omega$ , C=1uF)
45	NC	Not connect
46	VCOM	For external VCOM DC input
47	DITHB	Dithering setting
48	GND	Digital Ground
49	NC	Not connect
50	NC	Not connect

## 5. ABSOLUTE MAXIMUM RATINGS

### 5.1 Absolute maximum ratings

Item	Symbol	Values		Unit	Remark
		Min.	Max.		
Power voltage	$DV_{DD}$	-0.3	5.0	V	
	$AV_{DD}$	6.5	13.5	V	
	$V_{GH}$	-0.3	40.0	V	
	$V_{GL}$	-20.0	0.3	V	
	$V_{GH}-V_{GL}$	-	40.0	V	
Operation Temperature	$T_{OP}$	-30	85	°C	
Storage Temperature	$T_{ST}$	-30	85	°C	
LED Reverse Voltage	$V_R$	-	1.2	V	Each LED Note 2
LED Forward Current	$I_F$	-	25	mA	Each LED

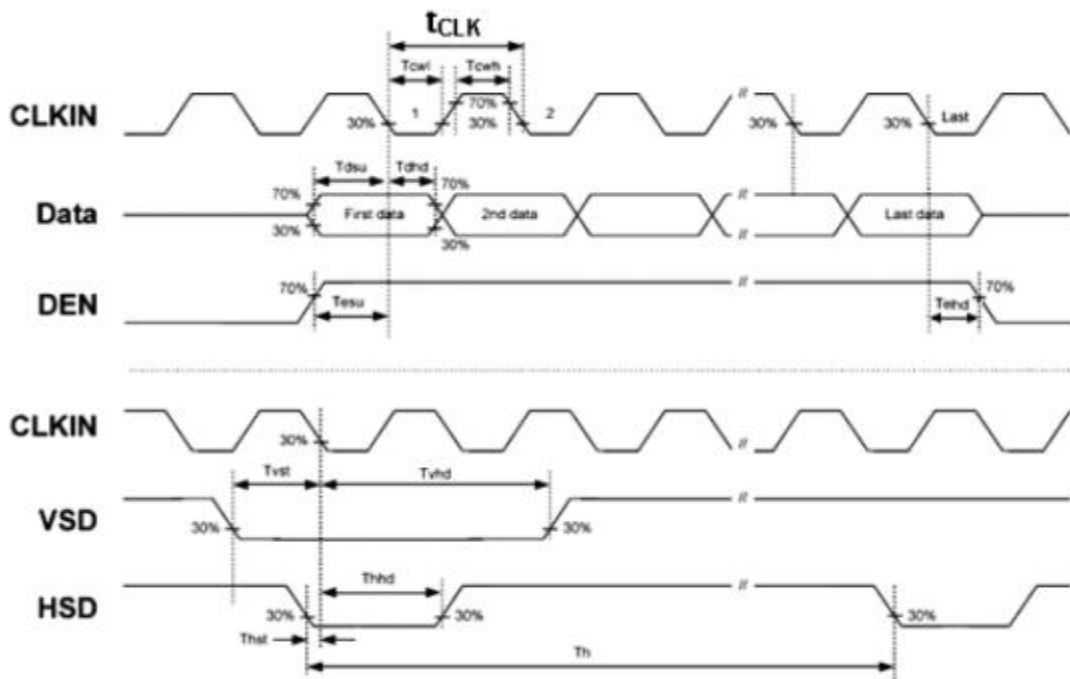
### 5.2 Typical operation conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	$DV_{DD}$	3.0	3.3	3.6	V	Note 2
	$AV_{DD}$	10.2	10.4	10.6	V	
	$V_{GH}$	15.3	16.0	16.7	V	
	$V_{GL}$	-7.7	-7.0	-6.3	V	
Input signal voltage	$V_{COM}$	3.6	3.8	4.0	V	
Input logic high voltage	$V_{IH}$	0.7 $DV_{DD}$	-	$DV_{DD}$	V	Note 3
Input logic low voltage	$V_{IL}$	0	-	0.3 $DV_{DD}$	V	

### 6. Timing Characteristics of input single

Input timing table

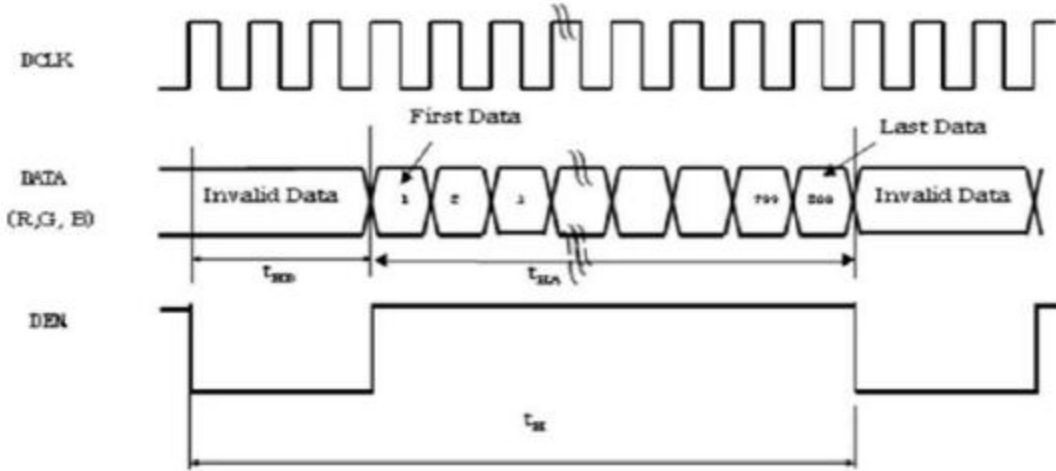
	ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	Note
DCLK	Dot Clock	$1/t_{CLK}$	29	33	38	MHZ	
	DCLK pulse duty	$T_{cwh}$	40	50	60	%	
DE	Setup Time	$T_{esu}$	8	-	-	ns	
	Hold time	$T_{ehd}$	8	-	-	ns	
	Horizontal Period	$t_H$	1026	1056	1086	$t_{CLK}$	
	Horizontal Valid	$t_{HA}$	800			$t_{CLK}$	
	Horizontal Blank	$t_{HB}$	226	256	286	$t_{CLK}$	
	Vertical Period	$t_V$	515	525	535	$t_H$	
	Vertical Valid	$t_{VA}$	480			$t_H$	
	Vertical Blank	$t_{VB}$	35	45	55	$t_H$	
SYNC	HSYNC Setup Time	$T_{hst}$	8	-	-	ns	
	HSYNC Hold Time	$T_{hhd}$	8	-	-	ns	
	VSYSN Setup Time	$T_{vst}$	8	-	-	ns	
	VSYSN Hold Time	$T_{vhhd}$	8	-	-	ns	
	Horizontal Period	$t_H$	1026	1056	1086	$t_{CLK}$	
	Horizontal Pulse Width	$t_{HPW}$	-	30	-	$t_{CLK}$	$t_{HB} + t_{HPW} = 46DCLK$ is fixed
	Horizontal Back Porch	$t_{HB}$	-	16	-	$t_{CLK}$	
	Horizontal Front Porch	$t_{HFP}$	180	210	240	$t_{CLK}$	
	Horizontal Valid	$t_{HV}$	800			$t_{CLK}$	
	Vertical Period	$t_V$	515	525	535	$t_H$	
	Vertical Pulse Width	$t_{VPW}$	-	13	-	$t_H$	$t_{VPW} + t_{VB} = 23t_H$ is fixed
	Vertical Back Porch	$t_{VB}$	-	10	-	$t_H$	
	Vertical Front Porch	$t_{VFP}$	12	22	32	$t_H$	
	Vertical Valid	$t_{VVD}$	480			$t_H$	
DATA	Setup Time	$T_{dsu}$	8	-	-	ns	
	Hold Time	$T_{dhd}$	8	-	-	ns	



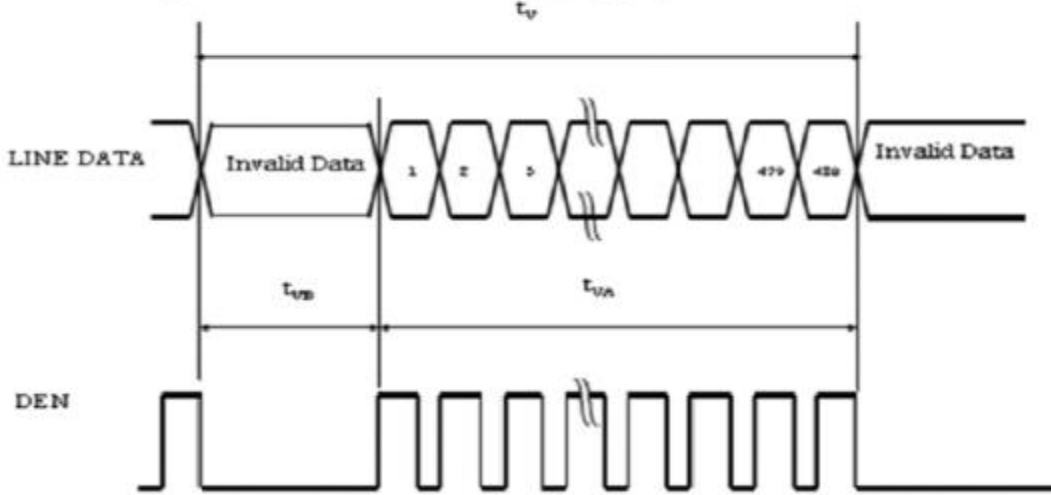
### 7. Timing sequence(timing chart)

#### 7.1 DE mode

Horizontal timing :



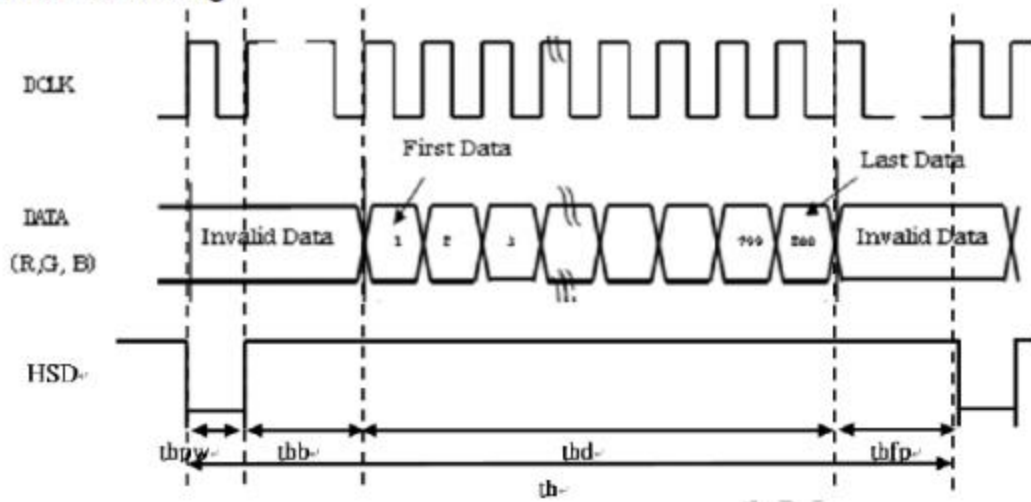
Vertical timing :



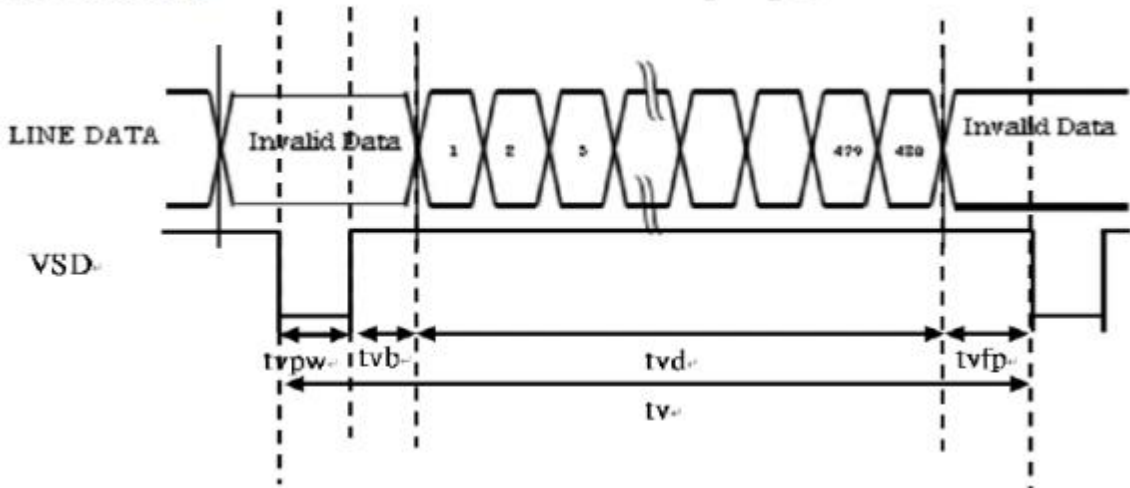


## 7.2 SYNC mode

Horizontal timing :

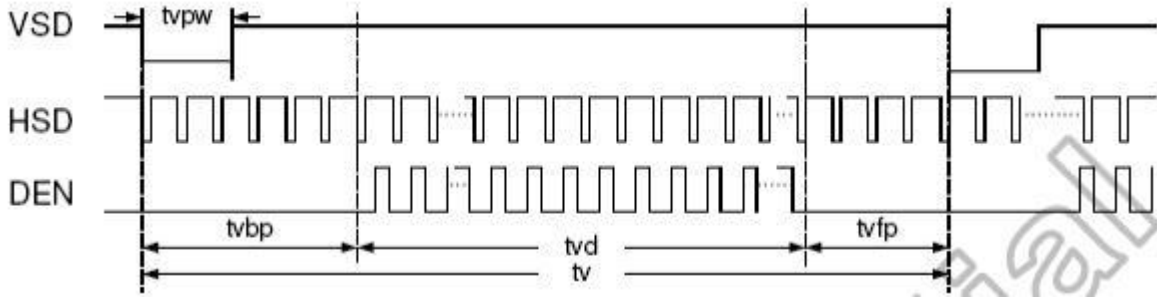


Vertical timing :

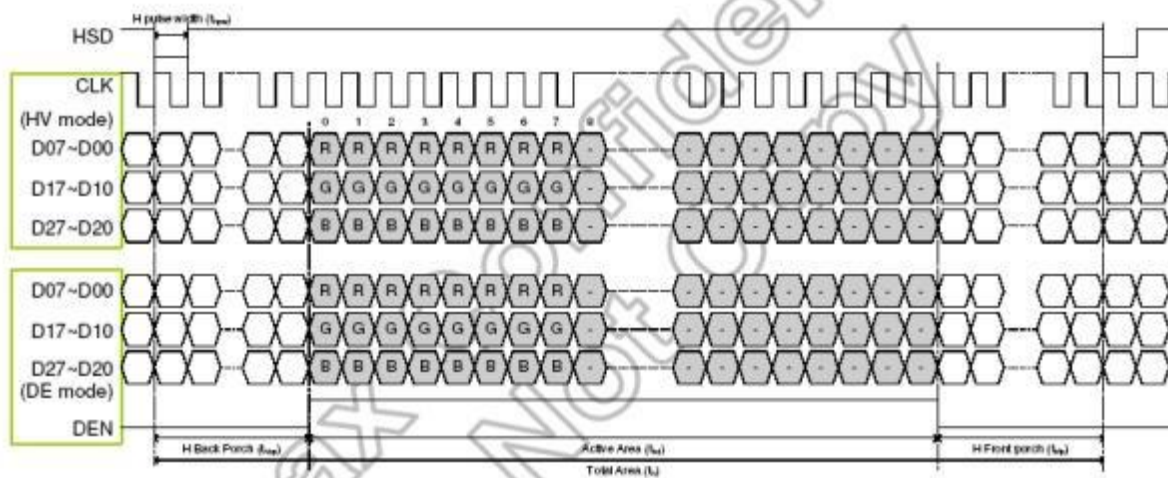


## 8. Data input format for RGB

### Vertical input Timing



### Horizontal input Timing



## 9. Backlight Characteristic

Item	Symbol	Min	Typical	Max	Unit
LED module Forward voltage	$V_{LED}$	--	9.6	--	V
LED module current	$I_{LED}$	--	120	--	mA
L/G Surface Luminance ★1	$L_S$	--	TBD	--	mcd
LCM Surface brightness uniform ★2	$L_D$	80	--	--	%

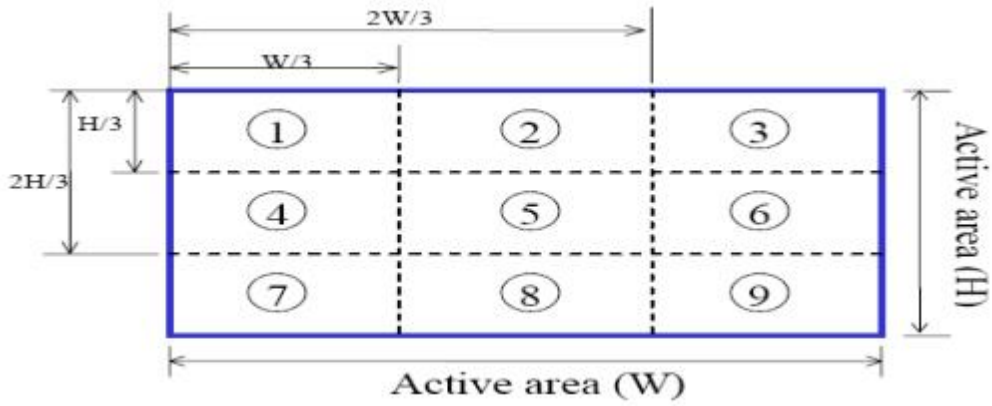
★ 1 Test condition is:

- (a) Center point on active area.
- (b) Best Contrast.

★2 Uniform measure condition:

- (1) Measure 9 point. Measure location show below;
- (2) Uniform = (Min. brightness / Max. brightness) \* 100%

(3)Best Contrast.



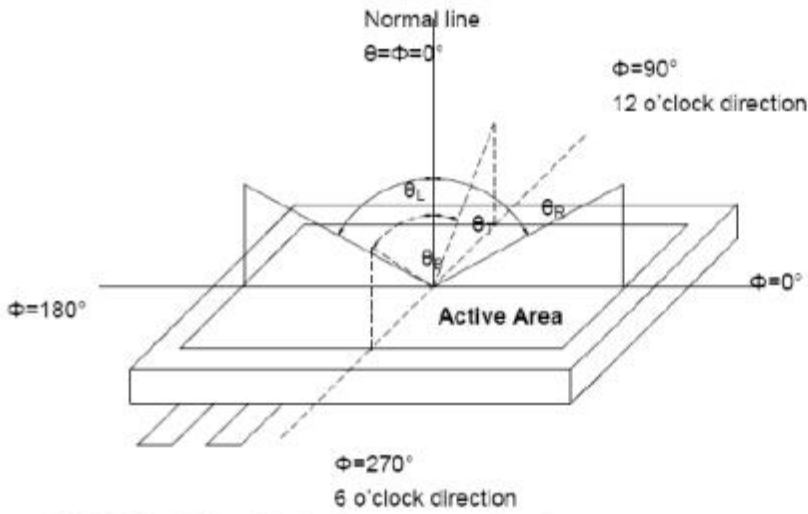
### 10.Electro-optical Characteristics

Item	Symbol	Condition	Values			Unit	Remark
			Min.	Typ.	Max.		
Viewing angle (CR≥ 10)	θL	Φ=180°(9 o'clock)	60	70	-	degree	Note 1
	θR	Φ=0°(3 o'clock)	60	70	-		
	θT	Φ=90°(12 o'clock)	40	50	-		
	θB	Φ=270°(6 o'clock)	60	70	-		
Response time	TON	Normal θ=Φ=0°	-	10	20	msec	Note 3
	TOFF		-	15	30	msec	Note 3
Contrast ratio	CR		400	500	-	-	Note 4
Color chromaticity (CF only, Base on C Light)	WX		0.278	0.308	0.338	-	Note 5
	WY		0.297	0.327	0.357	-	
Transmittance	Tr	-	-	5.11	-	%	

Test Conditions:

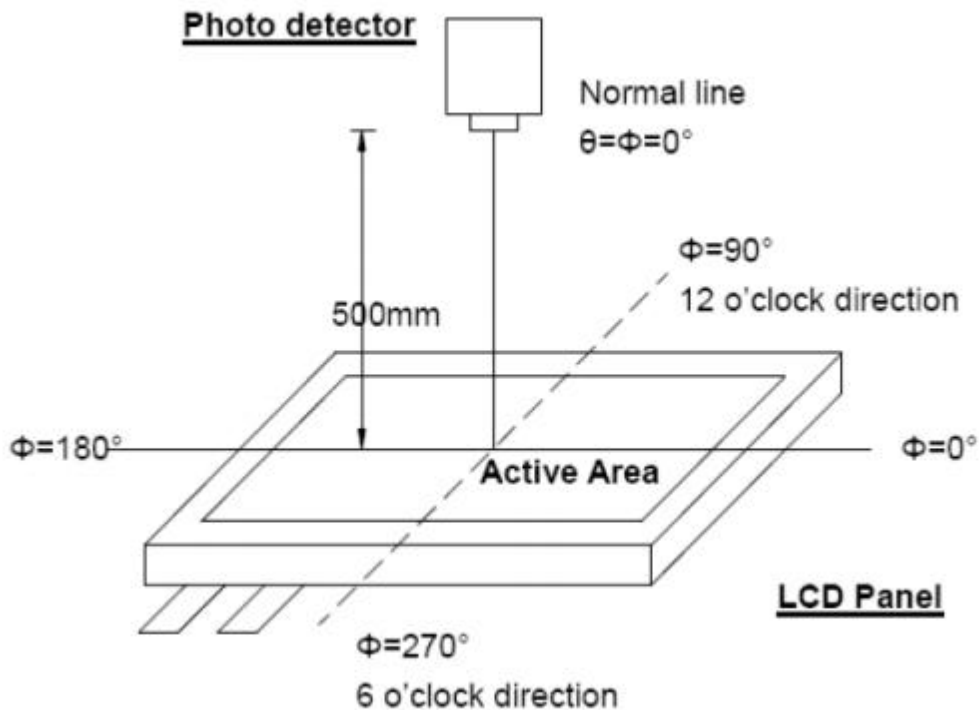
1. DVDD=3.3V, the ambient temperature is 25°C.
2. The test systems refer to Note 2.

Note 1: Definition of viewing angle range



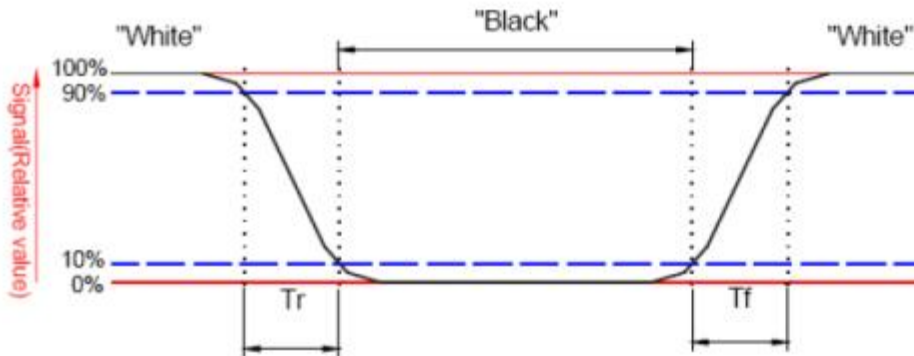
Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. The optical properties are measured at the center point of the LCD screen, (Response time is measured by Photo detector TRD\_100, other items are measured by BM-5A/Field of view :1°/Height 500mm.)



Note 3: Definition of response time:

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 4: Definition of contrast ratio:

Contrast ratio is calculated by the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the "white" state}}{\text{Brightness on the "black" state}}$$

Note 5: Definition of color chromaticity (CIE 1931)

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel.

## 11. Reliability

### 11.1 Test condition

Test	Test Condition	Judgment	Remark
High Temperature Storage Test	80°C, 240 hours	Note 1	Note2 Note3 Note4
Low Temperature Storage Test	-30°C, 240 hours	Note 1	
Thermal Shock Storage Test	-30°C, 0.5hour<->80°C, 0.5hour; 100cycles, 1hour/cycle	Note 1	
High Temperature Operation Test	70°C, 240 hours	Note 1	
Low Temperature Operation Test	-20°C, 240 hours	Note 1	
High Temperature &High Humidity Operation Test	60°C, 90%RH, 240hours	Note 1	

**Note1:** Criteria: Normal display image with no obvious non-uniformity and no line defect.

**Note2:** All tests above are practiced at module type.

**Note3:** All the cosmetic specification is judged before the reliability stress. Only a single item of these tests shall be executed on a single panel , not more than one test item shall be executed on a single panel.

**Note4:** Evaluation should be tested after storage at room temperature for two hours.

## 11.2 Shock and vibration

ITEMS	CONDITIONS
Shock (Non-Operation)	<ul style="list-style-type: none"> <li>● Shock level : 980m/s<sup>2</sup>(equal to 100G).</li> <li>● Waveform : 1/2 Sine wave,6msec</li> <li>● ±X · ±Y · ±Z · each axis 1 times</li> </ul>
Vibration (Non-Operation)	<ul style="list-style-type: none"> <li>● Frequency range : 8~33.3Hz</li> <li>● Stoke : 1.3 mm</li> <li>● Vibration : sinusoidal wave, perpendicular axis (both x, z axis:2Hrs, y axis 4Hrs).</li> <li>● Sweep : 2.9G, 33.3 Hz -400 Hz</li> <li>● Cycle : 15 min</li> </ul>

## 11.3 Electrostatic Discharge

TEST ITEM	CONDITIONS	NOTE
ESD	150pF · 330Ω · ±8kV&±15kV Air& Contact test	1
	200pF · 0Ω · ±200V Contact test	2

Note: Measure Point :

1. LCD glass and metal bezel
2. IF connector pins

## 12.Precautions for using LCD modules.

### 12.1 Safety

- (1)Do not swallow any liquid crystal ,even if there is no proof that liquid crystal is poisonous.
- (2)If the LCD panel breaks, be careful not to get liquid crystal to touch your skin.
- (3)If skin is exposed to liquid crystal, wash the area thoroughly with alcohol or soap.

### 12.2 Storage Conditions

- (4)Store the panel or module in a dark place where the temperature is  $23 \pm 5^{\circ}\text{C}$  and the humidity is below  $45 \pm 20\%RH$ .
- (5)Store in anti-static electricity container.
- (6)Store in clean environment, free from dust, active gas, and solvent.
- (7)Do not place the module near organics solvents or corrosive gases.
- (8) )Do not crush, shake, or jolt the module.

### 12.3Handling Precautions

- (9)Avoid static electricity, which can damage the CMOS LSI.
- (10)The polarizing plate of the display is very fragile, please handle if very carefully.
- (11)Do not give external shock.
- (12)DO not apply excessive force on the surface.
- (13)Bo not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- (14)Do not use ketonics solvent & Aromatic solvent, use with a soft cloth soaked with a cleaning naphtha solvent.
- (15)Do not operate it above the absolute maximum rating.

(16) Do not remove the panel or frame from the module.

## 12.4 Warranty

The period is within twelve months since the date of shipping out under normal using and storage conditions.